

Memorandum of Understanding
Centre of Excellence for VLSI and Embedded Systems
Hands-on Skill Development Program on Semiconductor Technologies
In Campus Establishment of Centre of Excellence of VLSI & Embedded Systems in BBDUTM in collaboration with PinE Training Academy (Training Division of Analog Technology, India)

This Memorandum of Understanding is signed on the 05th November, 2024 between the following institutions:

1. Mr. Vignesh Aridala – Founder/ CEO, PinE Training Academy (Training division of Analog Technology Private Limited) referred to as PTA/PTA, C/56/16, 2nd Floor, Sector-65, Noida, U.P. 201301, India
2. Dr. Madhukar Singh Chaturvedi – Director of BBDUTM, Aqueduct Road, Lucknow 226026, (referred to as BBDUTM hereafter).

This MOU is prepared to establish the foundation of the semiconductor technologies like ASIC Design & Full Custom Layouts, DFT, Physical Design, FPGA, Embedded PCB and ASIC Verification for IIT students of BBDUTM by PinE Training Academy LLP referred to as PTA/PTA, a training division of Analog Technology. Through this Memorandum of understanding, the two parties agreed to the following:

Scope of the MOU (General Terms):

1. PTA/PTA will run a core short-term skill development on VLSI in accordance with the technical content (Attached in Annexure-I) from July 2022 – July 2023 (for the 3-year academic session) during weekends/evenings or on public holidays after or before college hours/for free camp.
2. The course will be run for up to a maximum of 5 and maximum of 20 students per batch for each 3rd, 4th and 5th year batch on core electronics learning in BBDUTM Campus and training of 4th year will run in PTA/PTA campus (optional), learning content is attached for 3rd, 4th and 5th year in all of ASIC Design & Full Custom Layout, DFT, Physical Design, FPGA, Embedded PCB and ASIC verification referred to as D2).
3. BBDUTM will allow a maximum of 20 for interaction sessions with the 3rd to 4th year in every semester and with the 5th year in orientation on two days Digital Design and Verilog domains the final and HDL.
4. There is no financial commitment on the part of BBDUTM.

Commitments from the PTA/PTA side:

1. Trainee/Mentor from PTA/PTA or conduct the technical training on weekends or weekdays or on Public Holidays/leave.
2. Deliver then the interaction session, the learning session will be a maximum of 2hrs for each batch depending on the required technical content (with adequate breaks) 1 to 2 hours – at least one 20 minutes break.
3. PTA/PTA will run the course with up to 5 students on the BBDUTM campus.
4. After placement through PTA/PTA as an internship or full-time employee, the trainee's offer letter will share with the college DCE department and the College Training Placement cell.
5. PTA/PTA will work closely with the DCE department and Placement cell to place trainees in two domains.
6. PTA/PTA will share the training calendar in advance as well as the registration of students to the DCE department.

Commitments from BBDUTM side:

1. BBDUTM will provide the necessary infrastructure to set up Cell Lab as follows: Desktops/Computers, core training room, and a room equipped with chairs and tables, projector, power supply, etc.
2. BBDUTM will allow PTA/PTA to interact with students from 3rd year to 4th onwards through the domain, workshop, and lab sessions to get students for registration in Cell Lab.

Validity of the MOU

1. After validity as per the academic calendar, in case of termination, both parties PTA/PTA and BBDUTM will bring in notice and reason before and will consider one month's notice period before discontinuing.

Authorized Signatories:

PinE Training Academy

Vignesh Aridala
 Founder/CEO

Authorized Signatory

BBDUTM, Lucknow

Dr. Madhukar Singh Chaturvedi
 Director
 BBDUTM, Lucknow

PinE Training Academy LLP

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