

**RDDETE
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

Detail of FDPs attended for the duration 2014-15

S.No.	Name of Faculty	Title of FDP	Duration	Start Date	End Date	Name of Organization
1	Dr. Madhukar Satishrao	Finite Transient and Circuit Challenges in (SPICE)	3 days	12/08/2014	10/11/2014	VIT Vellore, Vellore Department of Microelectronics Design Cafe, Vellore, Chennai.
2	Dr. Madhukar Satishrao	Finite Transient and Circuit Challenges with param SPICE	3 days	1/7/2015	3/11/2015	RDDETE in collaboration with Laxmi and Shakti.
3	Mr. Rajeev Singh	Communication, Signal Processing and Recent Technologies for Different Applications	3 weeks	10/02/2014	10/10/2014	RDDE DET in collaboration with Laxmi and Shakti.
4	Mr. Rajeev Singh	Finite Transient and Circuit Challenges in (SPICE)	3 days	12/08/2014	10/11/2014	VIT Vellore, Vellore Department of Microelectronics Design Cafe, Vellore, Chennai.
5	Mr. Rajeev Singh	Design and development of Microelectronic Substrate	2 weeks	7/12/2014	7/20/2014	RDDE Group of Institutions
6	Mr. Rajeev Singh	Non-Linearities Technologies in Electronics and Computer Science: Funda and Teaching Approaches	3 days	6/06/2015	6/20/2015	Grand Welbels College of Engineering & Management
7	Dr. Rajesh Agarwal	Simulation and Empirical SPICE Tools & constraints with param SPICE	3 days	1/7/2015	3/11/2015	RDDE DET in collaboration with Laxmi and Shakti.
8	Dr. Rajesh Agarwal	Communication, Signal Processing and Recent Technologies for Different Applications	3 weeks	10/02/2014	10/10/2014	RDDE DET in collaboration with Laxmi and Shakti.
9	Dr. Rajesh Agarwal	Finite Transient and Circuit Challenges in (SPICE)	3 days	12/08/2014	10/11/2014	VIT Vellore, Vellore Department of Microelectronics Design Cafe, Vellore, Chennai.
10	Dr. Rajesh Agarwal	Design and development of Microelectronic Substrate	2 weeks	7/12/2014	7/20/2014	RDDE Group of Institutions
11	Dr. Rajesh Agarwal	Non-Linearities Technologies in Electronics and Computer Science: Funda and Teaching Approaches	3 days	6/06/2015	6/20/2015	Grand Welbels College of Engineering & Management
12	Dr. Rajesh Agarwal	Agilent SPICE Model, Empirical Simulation, Constraints	3 days	6/06/2015	6/20/2015	RDDE DET in collaboration with Laxmi and Shakti.
13	Mr. Anand Kumar Singh	Finite Transient and Circuit Challenges with param SPICE	3 days	1/7/2015	3/11/2015	RDDE DET in collaboration with Laxmi and Shakti.
14	Mr. Anand Kumar Singh	Communication, Signal Processing and Recent Technologies for Different Applications	3 weeks	10/02/2014	10/10/2014	RDDE DET in collaboration with Laxmi and Shakti.